

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,045,890 B2
APPLICATION NO. : 09/964494
DATED : May 16 2006
INVENTOR(S) : Xie et al.

Page 1 of 3

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

On Title Page, in field (56), under "U.S. Patent Documents", in column 2, line 1, delete "361/758" and insert --361/758 / 257/712 --, therefor.

On Title Page, in field (56), under "U.S. Patent Documents", in column 2, line 4, delete "361/704" and insert -- 361/704 / 361/705 --, therefor.

On Title Page, in field (56), under "U.S. Patent Documents" in column 2, line 14, delete "438/108" and insert -- 438/108 / 438/121 --, therefor.

On Title Page, in field (56), under "U.S. Patent Documents" in column 2, line 20, delete "Dibene et al." and insert -- Dibene, II, et al. --, therefor.

On Title Page, in field (56), under "U.S. Patent Documents", in column 2, line 21, delete "Dibene et al." and insert -- Dibene, II, et al. --, therefor.

On Title page, in field (56), under "U.S. Patent Documents" in column 2, line 22, delete "6,599,779 B1" and insert -- 6,599,779 B2 --, therefor.

On Title Page, in field (56), under "Other Publications", in column 2, line 2, after "(1992)" insert -- , --.

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In column 10, line 28, in Claim 1, delete “Inegrated” and insert -- Integrated --, therefor.

In column 10, line 43, in Claim 4, delete “Inegrated” and insert -- Integrated --, therefor.

In column 11, line 24, in Claim 16, delete “xircut” and insert -- circuit --, therefor.

In column 11, line 24, in Claim 16, delete “board;” and insert -- board (IC-PCB); --, therefor.

In column 11, line 40, in Claim, 18, delete “grid-array” and insert --grid array --, therefor.

In column 11, line 51, in Claim 20, after “component” delete “(DSC)”.

In column 12, line 35, in Claim 33, delete “IC” and insert -- integrated circuit --, therefor.

In column 12, line 46, in Claim 35, delete “IC” and insert -- integrated circuit --, therefor.

In column 12, line 49, in Claim 36, delete “IC” and insert -- integrated circuit --, therefor.

In column 13, line 25, in Claim 47, delete “claims 44, “ and insert -- claim 44, --, therefor.

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It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 13, line 49, in Claim 52, before "A carrier" delete "Withdrawn)".

In column 14, line 1, in Claim 55, delete "claims 1," and insert -- claim 1, --, therefor.

In column 14, line 18, in Claim 60, delete "coplanar" and insert -- co-planar --, therefor.

In column 14, line 61, in Claim 66, delete "mer" and insert -- stiffner portions form four corner --, therefor.

Signed and Sealed this

Twenty-sixth Day of September, 2006



JON W. DUDAS
Director of the United States Patent and Trademark Office